Google Scholar BETA

lpar modeling time interval resource CPU [Search]

Scholar   Results **1 - 10** of about **104** for **lpar modeling time interval resource CPU**. (0.19 seconds)

Multiple Operating Systems on One Processor Complex - group of 5 **All articles Recent articles**
»
TL Borden, JP Hennessy, JW Rymarczyk - IBM Systems Journal, 1989 - research.ibm.com
... capability, it may be difficult and **time**-consuming to ... equal (except for processors
on the **model** 500) partitions. **LPAR** gives the user the ability to define the ...
Cited by 10 - Related Articles - View as HTML - Web Search

A J2EE application for process accounting, **LPAR** accounting, and transaction accounting
CE Wu, WP Horn - Proceedings of the 5th international workshop on Software ..., 2005 - portal.acm.org
... The Web services provide the **model** with business functions ... each partition expands
and shrinks over **time** based on ... Figure 5 shows an **LPAR** accounting report for ...
Related Articles - Web Search

Dynamic reconfiguration of **CPU** and WebSphere on IBM pSeries servers - group of 3 »
J Jann, N Dubey, RS Burugula, P Pattnaik - Software Practice and Experience, 2004 - doi.wiley.com
... and developed to cover WebSphere's significantly expanding programming **model**. ... remove
the CPUs one at a **time** at the ... every 20 min) until the **LPAR** was running ...
Related Articles - Web Search - BL Direct

Modeling Host Environments using SNAP/SHOT - group of 3 »
G Crane, R Gutjahr, S Bugatti - redbooks.ibm.com
... 3.3.3 Two Servers and One **LPAR** Coupling Facilities ... viii **Modeling** Host Environments
using SNAP/SHOT ... Expectations: Users want improved response **time** and improved ...
View as HTML - Web Search

An Advanced Accounting Service for AIX Systems - group of 3 »
CE Wu, WP Horn - Services Computing, 2005 IEEE International Conference on, 2005 -
doi.ieeecomputersociety.org
... The backend **model** represents the accounting facility, its ... used by process-based
accounting and **LPAR**-based accounting ... response **time**, total queued **time**, and total ...
Related Articles - Web Search

Optimal Storage Allocation for Partitionable Processors
HP Artis - perfassoc.com
... **LPAR** 1 ... stated an ESTOR storage page movement requires 70-75 microseconds of **CPU time**.
7 ... unweighted **CPU** service unit: the number of processors on the **CPU model**. ...
View as HTML - Web Search

Update on **LPAR** and Parallel Sysplex Capacity Planning
AM Sherkow - Int. CMG Conference, 2000 - sherkow.com
... 10 Minute RMF **Interval** 2 CPs ... CICS TSO Batch STCs Other PGs MVS PGN=0 Uncaptured **Time**
Available ... The gross limit of an **LPAR**'s capacity is determined by the ...
Related Articles - View as HTML - Web Search - BL Direct

Using **interval** logics for temporal analysis of security protocols - group of 2 »
MR Hansen, R Sharp - Proceedings of the 2003 ACM workshop on Formal methods in ..., 2003 - portal.acm.org
... These examples show that in **interval** logics with dura- tions, real-**time** security
properties ... The basic **model** of shared processes given here originates from ...
Cited by 3 - Related Articles - Web Search